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CONFIRMATION NO. 7494

<b>SERIAL NUMBER</b> 10/671,539	<b>FILING OR 371(c) DATE</b> 09/29/2003 <b>RULE</b>	<b>CLASS</b> 438	<b>GROUP ART UNIT</b> 2818	<b>ATTORNEY DOCKET NO.</b> 0756-7204
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## APPLICANTS

Koichiro Tanaka, Atsugi, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-291545 10/03/2002 *Uji*IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\*  
12/17/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 11	<b>TOTAL CLAIMS</b> 51	<b>INDEPENDENT CLAIMS</b> 6
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ADDRESS  
31780

## TITLE

Method of manufacturing semiconductor device ✓

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